

1c978 U.S. PTO
09/910915



Class	Subclass	ISSUE CLASSIFICATION

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E. SCANNED <u>AC3</u> Q.A. <u>LA</u>	PATENT DATE
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APPLICATION NO. 09/910915	CONT/PRIOR F	CLASS 714	SUBCLASS 763	ART UNIT 2133	EXAMINER <u>Abraham</u> <u>Essau</u>
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APPLICANTS

Kouji Yamamoto

TITLE

Semiconductor memory device having ECC type error recovery circuit

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
INTERNATIONAL CLASSIFICATION							

☐ Continued on Issue Slip Inside File Jacket

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	_____ (Assistant Examiner) _____ (Date)			NOTICE OF ALLOWANCE MAILED	
	_____ (Primary Examiner) _____ (Date)			ISSUE FEE Amount Due Date Paid	
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